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Information Disclosure
Statement By Applicant

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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
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PW	2	4,104,718	08/1978	Poublan et al.	364	200	
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Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
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PW	27	Austin et al., "Efficient detection of all pointer and array access errors" 1993, Computer Sciences Dept., Univ. of Wisconsin-Madison, pp. 1-29.
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PW	29	Boothe, "Fast accurate simulation of large shared memory multi-processors", 1988, US Berkeley EECS Rpt. No. UCB/CDS 92/682.